

	Application No.	Applicant(s)
	10/786,204	MATHUR ET AL.
Notice of Allowability Ex	Examiner	Art Unit
	James Cha	2819
	James Cho	2819
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIP of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comm GHTS. This application is	in this application. If not included nunication will be mailed in due course. THIS
1. $\boxtimes$ This communication is responsive to <u>the response filed Ma</u>	<u>y 30, 2006</u> .	
2. ☑ The allowed claim(s) is/are <u>4 and 6-11</u> .		
3. Acknowledgment is made of a claim for foreign priority un	der 35 U.S.C. § 119(a)-(d)	or (f).
a) ☐ All b) ☐ Some* c) ☐ None of the:		
1.  Certified copies of the priority documents have	been received.	
2.   Certified copies of the priority documents have	been received in Applicati	on No
<ol><li>Copies of the certified copies of the priority doc</li></ol>	cuments have been receive	ed in this national stage application from the
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	ENT of this application.	
<ol> <li>A SUBSTITUTE OATH OR DECLARATION must be submi INFORMAL PATENT APPLICATION (PTO-152) which give</li> </ol>		
5. X CORRECTED DRAWINGS ( as "replacement sheets") mus	t be submitted.	
(a) $\square$ including changes required by the Notice of Draftspers	on's Patent Drawing Revie	w ( PTO-948) attached
1) ☐ hereto or 2) ☑ to Paper No./Mail Date <u>9/1/05</u> .		
(b) including changes required by the attached Examiner's Paper No./Mail Date	Amendment / Comment o	or in the Office action of
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the		
<ol> <li>DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT F</li> </ol>		
Attachment(s)		
1. Notice of References Cited (PTO-892)		nformal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	Paper No	Summary (PTO-413), ./Mail Date
<ol> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/06)</li> <li>Paper No./Mail Date 9/06</li> </ol>		s Amendment/Comment
<ol> <li>Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ol>		s Statement of Reasons for Allowance
	9. 🗌 Other	•

# **DETAILED ACTION**

#### Continued Examination Under 37 CFR 1.114

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under *Ex Parte Quayle*, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on 9-21-2006 has been entered.

### **Drawings**

In order to avoid abandonment, the drawing informalities noted in the paper mailed on September 1, 2005, must now be corrected. Correction can only be effected in the manner set forth in the above noted paper.

### Allowable Subject Matter

Claims 4 and 6-11 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: Although Lacey et al. (US PAT No. 5,485,107) a backplane driver circuit, one of ordinary skill in the art would not have been motivated to modify the teaching of Lacey et al. to further includes, among other things, the specific of the input buffer circuit being configured to prevent an over-voltage condition on each of the plurality of transistors (claim 4), the specifics of a first and second pull-up transistor coupled in series where the second pull-up transistor having a gate biased by the first reference voltage (claim 6), the first bias

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voltage being approximately 1.1 volts during the normal mode and input minus two diode drops during the hot-plug mode (claim 10), and the second bias voltage being approximately 2.5 volts during the normal mode and approximately the input minus two diode drops during the hot-plug mode (claim 11).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

# Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Cho whose telephone number is 571-272-1802. The examiner can normally be reached on M-F 6:30 AM - 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rexford Barnie can be reached on 571-272-74927492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James Cho

Primary Examiner Art Unit 2819